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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant **Luigi Colombo**

Docket Number: **TI-36145**

Serial No.: **10/686,011**

Art Unit: **2812**

Filed: **10/14/03**

Examiner: **Walter L. Lindsay, Jr.**

For: **Encapsulated MOS Transistor Gate Structures and
Methods for Making the Same**

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile
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Karen Vertz
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11-11-04
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FACSIMILE COVER SHEET

☒ FACSIMILE COVER SHEET	☐ AMENDMENT _____ (# Pages) _____
☐ NEW APPLICATION	☐ EOT
☐ DECLARATION	☐ NOTICE OF APPEAL
☐ ASSIGNMENT	☐ APPEAL _____ (# Pages) _____
☐ FORMAL DRAWINGS	☐ ISSUE FEE & PUBLICATION FEE (1 Page)
☐ INFORMAL DRAWINGS	☐ REPLY BRIEF (IN TRIPLICATE) (# Pages)
☐ CONTINUATION APPN	☒ ELECTION
☐ DIVISIONAL APPN	
NAME OF INVENTOR(S): Luigi Colombo	
TITLE OF INVENTION: Encapsulated MOS Transistor Gate Structures and Methods for Making the Same	
TI FILE NO.: TI-36145	DEPOSIT ACCT. NO.: 20-0668
FAXED: 11-11-04 DUE: 12-2-04 ATTY/SECY: RAK/kjv	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/686,011 Filing Date: 10/14/03	

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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ELECTION

This election is offered in response to the Examiner's restriction requirement mailed November 2, 2004.

Applicants hereby provisionally elect to pursue the first invention with Claims 30-34 without traverse.

Respectfully submitted,

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